



highstreet
technologies
Network solutions

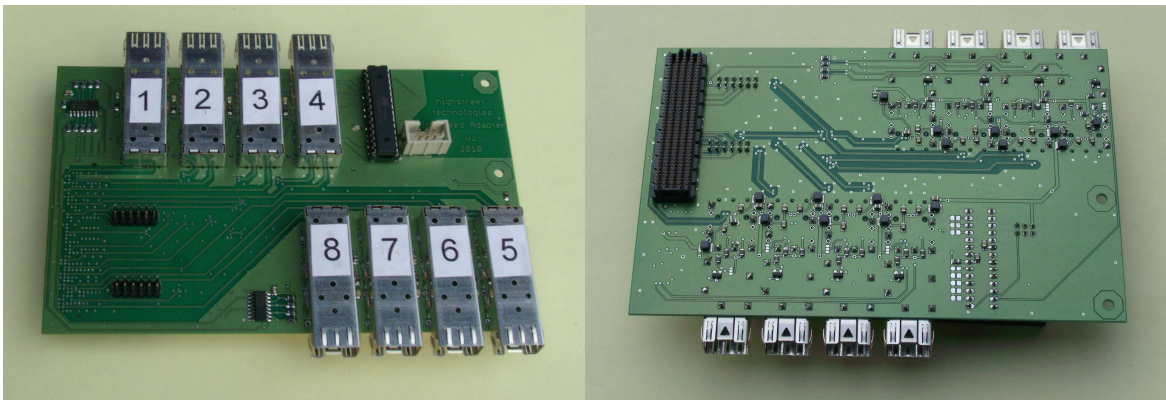
highstreet ML605 Interface Adapter Product Flyer

OVERVIEW

Xilinx' Virtex-6 FPGA ML605 Evaluation Kit is well suited for testing digital circuitry on Xilinx Virtex-6 FPGA. Whereas the Virtex-6 family members dispose of a high amount of high speed interfaces, the ML605 Evaluation Kit provides external access to just one of them. The others are only accessible via the FMC connector. Often, however, tests require access to many more interfaces of the Virtex-6 FPGA. This is where the highstreet ML605 Interface Adapter comes in handy. This module can be plugged into the FMC socket, thus providing full access to up to eight high-speed interfaces of the Virtex-6 FPGA.

IMPLEMENTATION

The highstreet ML605 Interface Adapter is used as a daughterboard of Xilinx' Virtex-6 FPGA ML605 Evaluation Kit. It is plugged into the FMC socket of the ML605 board. The external interfaces support data rates of 650 Mbit/s till 3.2 Gbit/s including mainstream industry-standard protocols like PCI Express or telecoms protocols like Gigabit Ethernet, CPRI, and OBSAI. Different SFP modules can be inserted into the eight cages. The SFP modules are controllable via I²C. Another 20 pins can be used for test purposes. Power is supplied by the Virtex-6 FPGA ML605 Evaluation Kit.



highstreet ML605 Interface Adapter (front and rear view)

TECHNICAL DATA

Network Simulation Package	Deliverables
Interfaces	8 interfaces of industry-standards protocols like PCI Express and telecoms protocols like Gigabit Ethernet, CPRI, and OBSAI at data rates of 650 Mbit/s till 3.2 Gbit/s
SFP Module Types	arbitrary SFP modules compatible with specific protocol (650 Mbit/s till 3.2 Gbit/s) 10 / 100 Mbit/s Ethernet with SFPs supporting SGMII
Control Interfaces	1 x I ² C 20 configurable test pins
Dimensions	160 mm x 100 mm x 19 mm
Power Supply	via Virtex-6 FPGA ML605 Evaluation Kit